

### Status of Claims

Claims 1-20 cancelled.

1           **21. (new)      A computer system comprising:**  
2           **a power supply for providing a voltage;**  
3           **at least two boards, each board receiving the voltage, and wherein each**  
4           **board comprises**  
5           **at least one voltage regulator, for receiving the voltage and for providing a**  
6           **regulated voltage level to the board, and**  
7           **at least one processor for controlling the regulated voltage level,**  
8           **wherein the processor monitors a value of at least one power-related**  
9           **parameter on the board and controls the voltage regulator in such a way as to**  
10          **influence a subsequent value of the at least one parameter.**

1           **22. (new)      The computer system of claim 21 wherein the processor,**  
2           **upon detection of a fault associated with the at least one power related parameter,**  
3           **shuts down the board.**

1           **23. (new)      The computer system of claim 21 wherein the at least one**  
2           **power-related parameter is a regulated voltage of the board.**

1           **24. (new)      The computer system of claim 21 wherein the at least one**  
2           **power-related parameter is a temperature value of the board.**

1           **25. (new)      The computer system of claim 21 wherein each board further**  
2           **comprises a signaling interface for receiving instructions therefrom, and wherein,**  
3           **the processor is responsive to the received instructions for controlling the at least**  
4           **one voltage regulator.**

1           **26. (new)**     The computer system of claim 25 wherein the processor  
2 causes data to be written to a system log file, wherein the data is associated with the  
3 at least one power-related parameter.

1           **27. (new)**     The computer system of claim 21 further comprising an  
2 interface for coupling to a console for receiving instructions therefrom for  
3 controlling various ones of the processors on each of the at least two boards.

1           **28. (new)**     The computer system of claim 23 wherein the processor  
2 collects temperature values over time for performing a time-based analysis of the  
3 collected temperature values.

1           **29. (new)**     A computer system comprising:

2           a plurality of boards, each board comprising a power control element,  
3 wherein the power control element comprises a regulator for providing a regulated  
4 voltage to the board and a processor for monitoring and controlling the regulator;  
5 and

6           a signaling interface coupled to each power control element of each of the  
7 plurality of boards for communicating data to, and from, each one of the  
8 processors,

9           wherein the processor for each board monitors a value of at least one  
10 power-related parameter for its board and controls its regulator in such a way as to  
11 influence a subsequent value of the at least one parameter.

1           **30. (new)**     The computer system of claim 29 wherein the processor for  
2 each board, upon detection of a fault associated with the at least one power related  
3 parameter, shuts down its board.

1           **31. (new)**     The computer system of claim 29 wherein the at least one  
2 power-related parameter is a regulated voltage of the board.

3           **32. (new)**     The computer system of claim 29 wherein the at least one  
4 power-related parameter is a temperature value of the board.

1           **33. (new)**     The computer system of claim 29 wherein the processor for  
2 each board is responsive to instructions received from the signaling interface for  
3 controlling its regulator.

1           **34. (new)**     The computer system of claim 29 wherein the processor for  
2 each board causes data to be written to a system log file via the signaling interface  
3 and wherein the data is associated with the at least one power-related parameter of  
4 its board.

1           **35. (new)**     The computer system of claim 29 further comprising an  
2 interface for coupling to a console for receiving instructions therefrom for  
3 controlling various ones of the processors on each board.

1           **36. (new)**     The computer system of claim 29 further comprising a  
2 central controller coupled to the signaling interface for controlling the processor on  
3 each of the plurality of boards.

1           **37. (new)**     The computer system of claim 36 wherein the central  
2 controller causes data to be written to a log file representative of information  
3 received, via the signaling interface, with respect to at least one power-related  
4 parameter of one of the plurality of boards.

1           **38. (new)**     The computer system of claim 36 further comprising an  
2 interface for coupling the central controller to a console for receiving instructions  
3 therefrom for controlling various ones of the processors on each board.